



CHALAPTHI INSTITUTE OF TECHNOLOGY

Electrical and Electronics Engineering Department

Academic Year 2016-17

Time Table

Class: II-B.Tech EEE

Semester:II

LH.NO:SF-26

W.E.F: 00/00/2017.

Period /day	1	2	3	4	5	1.10-2.00	6	7	8
	9.00-9.50	9.50-10.40	10.40-11.30	11.30-12.20	12.20-1.10		2.00-2.50	2.50-3.40	3.40-4.30
MON	CS	STLD	EM-II	PS-I	ES	L U N C H	PDC	STLD	SPORTS
TUE	EM-II	PDC	CS	ES	EM-II		PS-I	CS	STLD
WEN	EM-I LAB				CS		PDC	PS-I	EM-II
THU	ES	PS-I	CS	STLD	PDC		EM-II	ES	PS-I
FRI	EM-II	PDC	PS-II	STLD	ES		STLD	PS-I	CS
SAT	EDC LAB				EM-II		CS	PDC	ES

Subject code	Subject name	Name of the Faculty	Subject code	Subject name	Name of the Faculty
EM-II	Electrical Machines-II	K.KIRANMAI	PDC	Pulse & Digital Circuits	A.GIRIDHAR
PS-I	Power Systems-I	T.VENKATESH	EM-I LAB	Electrical Machines-I Lab	K.KIRANMAI/ T.VENKATESH
CS	Control Systems	K.KATYAYINI	EDC LAB	Electronic Devices & Circuits Lab	A.GIRIDHAR/ CH.ANIL BABU
ES	Environmental Studies	P.USHA RANI	LIB		
STLD	Switching Theory and Logic Design	A.LALITHA	SPORTS		

Class In-Charge

Time Table Coordinator

Head of The Department