



CHALAPATHI INSTITUTE OF TECHNOLOGY

ELECTRICAL AND ELECTRONICS Engineering Department

Academic year 2016-17

Time Table

Class: I –B.Tech EEE

Semester: II

L.H.No.SF-07

W.E.F.21-12-2016

| Period/ Day | 1 | 2 | 3 | 4 | 12:20-1:10 | 5 | 6 | 7 | 8 |
|----------------|-----------|------------|-------------|-------------|--|--------------|-----------|-----------|-----------|
| | 9:00-9:50 | 9:50-10:40 | 10:40-11:30 | 11:30-12:20 | | 1:10-2:00 | 2:00-2:50 | 2:50-3:40 | 3:40-4:40 |
| Monday | M-3 | ENG | AP | ECA | L U N C H B R E A K | AP LAB/IT/WS | | | M-2 |
| Tuesday | M-2 | M-2 | M-3 | ENG | | ED | | | |
| Wednesday | ECA | M-3 | AP | M-2 | | M-3 | M-2 | ECA | AP |
| Thursday | M-2 | ECS LAB | | | | M-3 | ENG | LIB | ECA |
| Friday | AP | M-3 | ENG | ECA | | AP LAB/IT/WS | | | M-3 |
| Saturday | AP | M-2 | ECA | AP | | ED | | | |
| | | | | | | | | | |

| Subject code | Subject Name | Name of the faculty |
|--------------|-------------------------------------|-------------------------------|
| ENG | ENGLISH-II | B.TULASI RANI |
| M-3 | MATHEMATICS-III | S.VENKAYAMMA |
| AP | APPLIED PHYSICS | V.NAGAMALLIKA/V.C.ANADARAO |
| M-2 | MATHEMATICS-II | SD.KURSHEED JAHAN BEGUM |
| ECA | ELECTRICAL CIRCUIT ANALYSIS | P.ANUSHA |
| ED | ENGINEERING DRAWING | M.KRISHNA CHAITANYA/S.RAMARAO |
| ECS LAB | ENGLISH COMMUNICATION SKILLS LAB-II | B.TULASI RANI/V.V.SUBBARAO |
| AP LAB | APPLIED PHYSICS LAB | V.NAGAMALLIKA |
| IT/WS | IT/ ENGINEERING WORKSHOP | J.SRAVANTHI / PAPANIDAS |

Class In-Charge

Time Table Coordinator

Head of the Department