## **Department of ECE**

Name:Y ROJA

**Designation**: Assistant Professor

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**Qualification**: M.Tech.

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## Faculty(Details)

## **Workshops Attended/Coordinated:**

1. Participated in a Workshop on "VLSI Circuits Analog and Digital Design Perspectives Conducted by Department of Electronics & Communication Engineering in Association with AMS Semiconductors & HCL Technologies from 16/05/2020 to 17/05/2020.

## **Webinars Attended:**

- 1. Participated in one day Webinar on "VLSI Design and Job Opportunities in Semiconductor Industry" on 26-05-2020.
- 2. Participated in Two day Webinar on "Image Processing and Computer Vision-Industrial Scope " on 27-05-2020 and 28-05-2020 By Dr.Karthik Seemakurthy research Scientist in TCS innovation & Research Lab.