Department of ECE

Name: SUPRIYA JANGALA

Designation: Assistant Professor

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Qualification: M.Tech.

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Faculty(Details)

FDP's Attended/Coordinated:

1. Participated in AICTE Sponsored one week STTP on **VLSI Design and Verification** from 19.04.2021 to 24.04.2021 Organised by Department of Electronics and Communication Engineering ,Annamacharya Institute of Technology and Sciences,Rajampet.

2. Participated in One Week National Level Faculty Development Program on (FDP) on "Artificial Intelligence" Conducted from 24-05-2020 to 29-05-2020.

Workshops Attended/Coordinated:

1. Participated in a Workshop on " VLSI Circuits Analog and Digital Design Perspectives Conducted by Department of Electronics & Communication Engineering in Association with AMS Semiconductors & HCL Technologies from 16/05/2020 to 17/05/2020.

Webinars Attended/Coordinated:

1. Participated in one day Webinar on " VLSI Design and Job Opportunities in Semiconductor Industry" on 26-05-2020.

2. Participated in Two day Webinar on "Image Processing and Computer Vision-Industrial Scope " on 27-05-2020 and 28-05-2020 By Dr.Karthik Seemakurthy ,research Scientist in TCS innovation & Research Lab.

3. Participated in one day Seminar on " Role of Internships and Industrial Projects in Engineering Education " Organised by V R Siddhartha Engineering College ,Vijayawada on 11th November 2021.